

AMENDMENTS TO THE CLAIMS:

1. (Currently amended) An image sensing apparatus, comprising:
an image quality mode setting block for setting only one of a plurality of image
quality modes;
a solid state image sensing element for converting an optical image into an electric
analog signal; and
an AD conversion block for converting the analog signal output from the solid state
image sensing element, into element into a digital signal with a quantization bit count
corresponding only to an the one image quality mode set by the image quality mode selection
setting block.
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2. (Currently amended) The image sensing apparatus as claimed in Claim 1, wherein the
AD conversion block is comprises an AD converter for selecting one of a plurality of
quantization bit eount counts corresponding to the image quality mode.

3. (Currently amended) The image sensing apparatus as claimed in Claim 1, wherein the
AD conversion block is realized by comprises a plurality of AD converters having different
quantization bit counts, and the apparatus further comprises an AD converter selection block
for selecting one of the AD converters to be activated and the other AD converters to be
maintained inactive, the selected one of the AD converters corresponding to the image quality
mode set by the image quality mode setting block.

4. (Original) The image sensing apparatus as claimed in Claim 1, wherein a higher image quality mode corresponds to a greater bit count and a lower image quality mode corresponds to a smaller bit count.

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5. (Currently amended) The image sensing apparatus as claimed in Claim 1, the apparatus further comprising a digital signal processing block for performing ~~an~~ image processing ~~to~~ ~~of~~ the digital signal output from the AD conversion block, ~~with~~ the digital signal processing block having a quantization bit count corresponding to ~~an~~ the image quality mode set by the image quality mode setting block.

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6. (Original) The image sensing apparatus as claimed in Claim 5, wherein the digital signal processing block sets a corresponding quantization bit count when an image quality mode is set by the image quality mode setting block.

7. (Currently amended) The image sensing apparatus as claimed in Claim 5, the apparatus further comprising a data thinning block for thinning the output signal from the digital signal processing block.

8. (Currently amended) The image sensing apparatus as claimed in Claim 7, wherein the digital signal processing block performs ~~at least~~ a color interpolation processing.

9. (Currently amended) The image sensing apparatus as claimed in Claim 5, the

apparatus-further comprising: an comprising an image storage block for storing the output signal from the digital signal processing block as an image data; and data.

~~an image display block for displaying the image according to the output signal from the digital signal processing block, the image display block having a function to select whether to display the image while the image data is being stored in the image storage block.~~

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10. (Currently amended) The image sensing apparatus as claimed in Claim 5, the apparatus further comprising:

an image storage block for storing the output signal from the digital signal processing block as an image data; and

an image display block for displaying ~~the~~ an image according to the output signal from the digital signal processing block,

wherein the solid state image sensing element, the AD conversion block, and the digital signal processing block stop ~~their~~ operations operation while the image display block is displaying an image according to the image data stored in the image storage block.

11. (Currently amended) The image sensing apparatus as claimed in Claim 1, wherein the image sensing apparatus ~~is realized as~~ comprises an electronic still camera.

12. (New) The image sensing apparatus as claimed in Claim 1, wherein said AD conversion block receives a bit count conversion signal from said image quality mode setting block, and converts the analog signal based on the bit count conversion signal.

13. (New) The image sensing apparatus as claimed in Claim 9, further comprising an image display block for displaying an image according to the output signal from the digital signal processing block, the image display block selecting whether to display the image during storage of image data in the image storage block.

14. (New) An image sensing apparatus, comprising:

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an image quality mode setting block for setting one of a plurality of image quality modes;

a solid state image sensing element for converting an optical image into an electric analog signal; and

an AD conversion block for converting the analog signal from the solid state image sensing element into a digital signal, the AD conversion block having a plurality of quantization bit counts corresponding respectively to the plurality of image quality modes, wherein the AD conversion block is responsive to the image quality mode set by the image quality mode setting block to enable the quantization bit count corresponding to the set image quality mode and disables other quantization bit counts.

15. (New) The image sensing apparatus as claimed in Claim 14, wherein the AD conversion block comprises an AD converter having a plurality of quantization bit counts corresponding respectively to the image quality modes, wherein the image quality mode setting block selects one of the quantization bit counts.

16. (New) The image sensing apparatus as claimed in Claim 14, wherein a higher image quality mode corresponds to a greater bit count and a lower image quality mode corresponds to a smaller bit count.

17. (New) The image sensing apparatus as claimed in Claim 14, further comprising a digital signal processing block for performing image processing of the digital signal from the AD conversion block.

18. (New) The image sensing apparatus as claimed in Claim 17, wherein the digital signal processing block sets a corresponding quantization bit count when an image quality mode is set by the image quality mode setting block.

19. (New) The image sensing apparatus as claimed in Claim 17, further comprising a data thinning block for thinning the output signal from the digital signal processing block.

20. (New) The image sensing apparatus as claimed in Claim 19, wherein the digital signal processing block performs color interpolation processing.

21. (New) The image sensing apparatus as claimed in Claim 17, further comprising: an image storage block for storing the output signal from the digital signal processing block as image data.

22. (New) The image sensing apparatus as claimed in Claim 21, further comprising an image display block for displaying the image according to the output signal from the digital signal processing block, the image display block selecting whether to display the image during storage of the image data in the image storage block.

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23. (New) The image sensing apparatus as claimed in Claim 17, further comprising:
an image storage block for storing the output signal from the digital signal processing block as image data; and
an image display block for displaying the image according to the output signal from the digital signal processing block,
wherein the solid state image sensing element, the AD conversion block, and the digital signal processing block stop operation during displaying by the image display block of an image based on image data stored in the image storage block.

24. (New) The image sensing apparatus as claimed in Claim 14, wherein the image sensing apparatus comprises an electronic still camera.

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AMENDMENTS TO THE DRAWINGS

Please amend the designation of Fig. 4(d) to Fig. 4(a) and the designation of Fig. 4(e) to Fig. 4(b).